

Fig.1

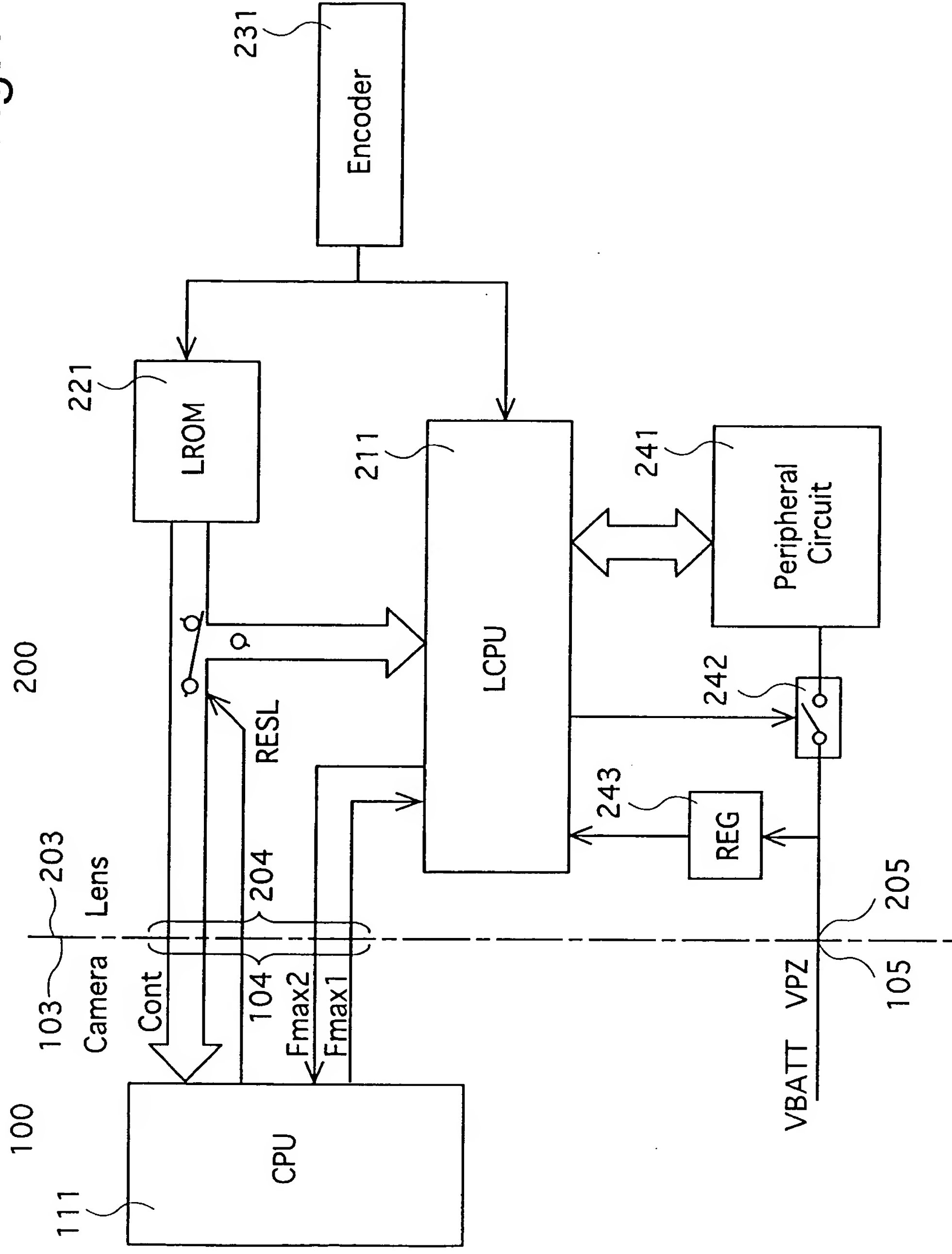
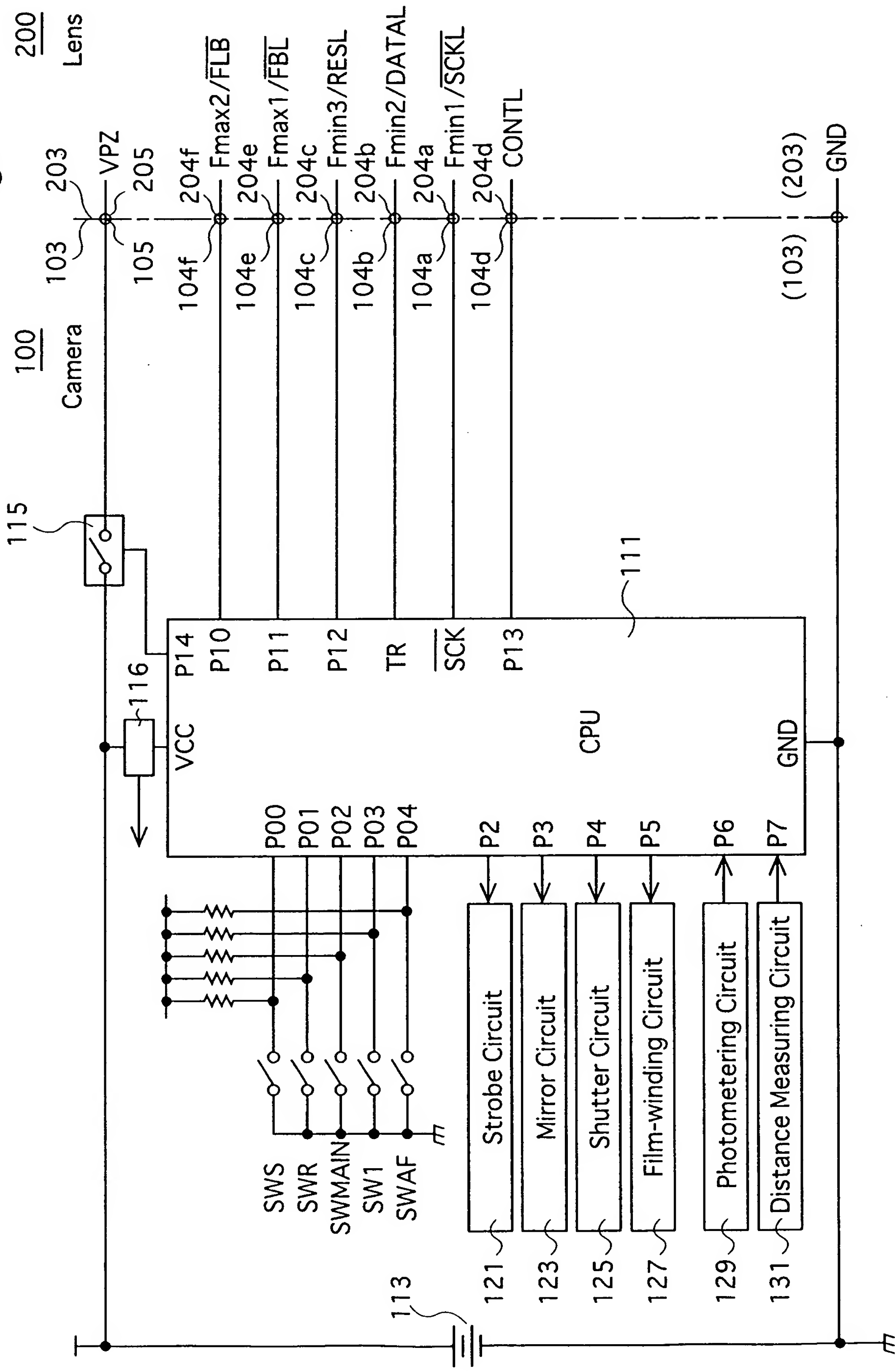
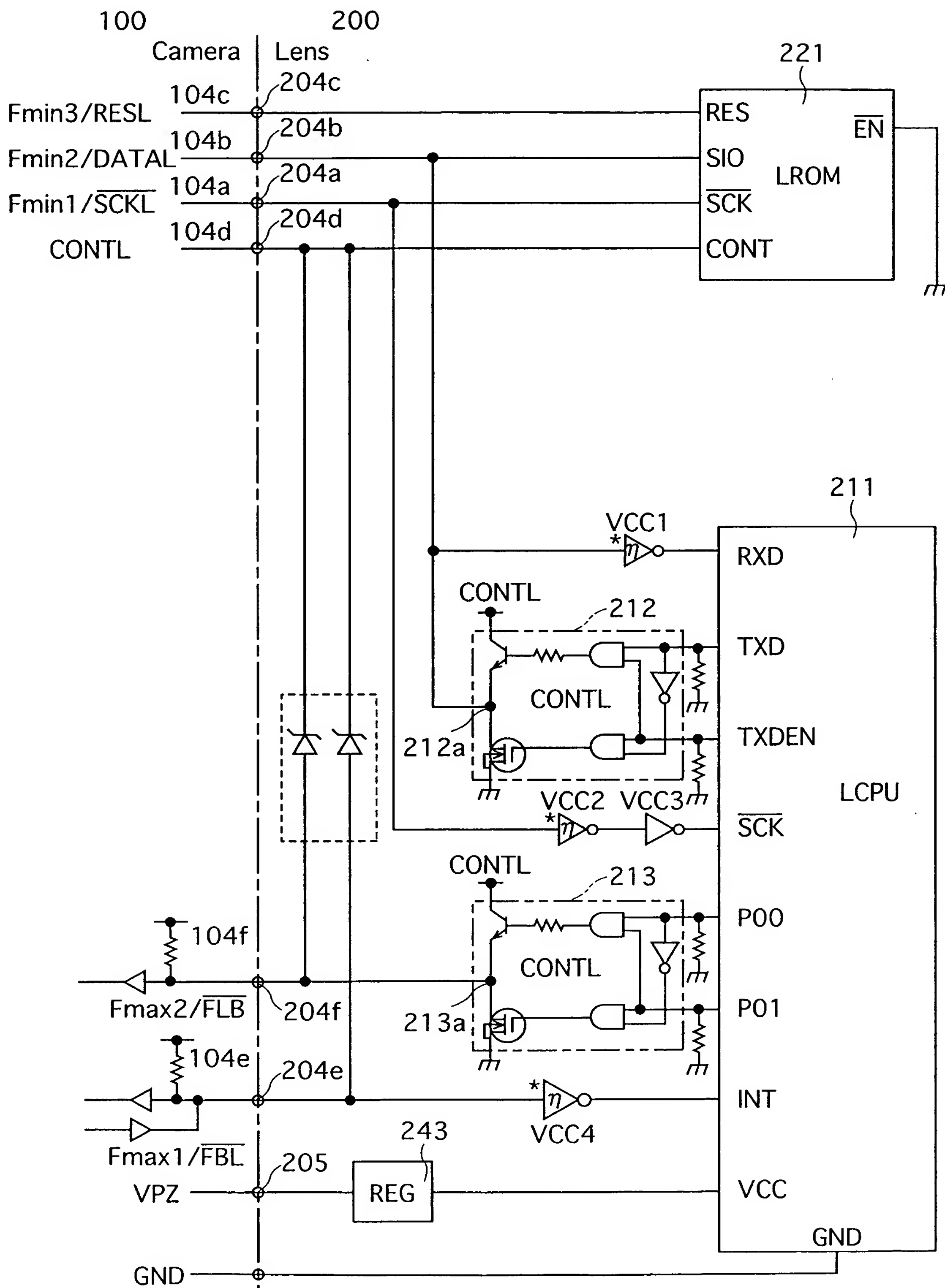


Fig.2

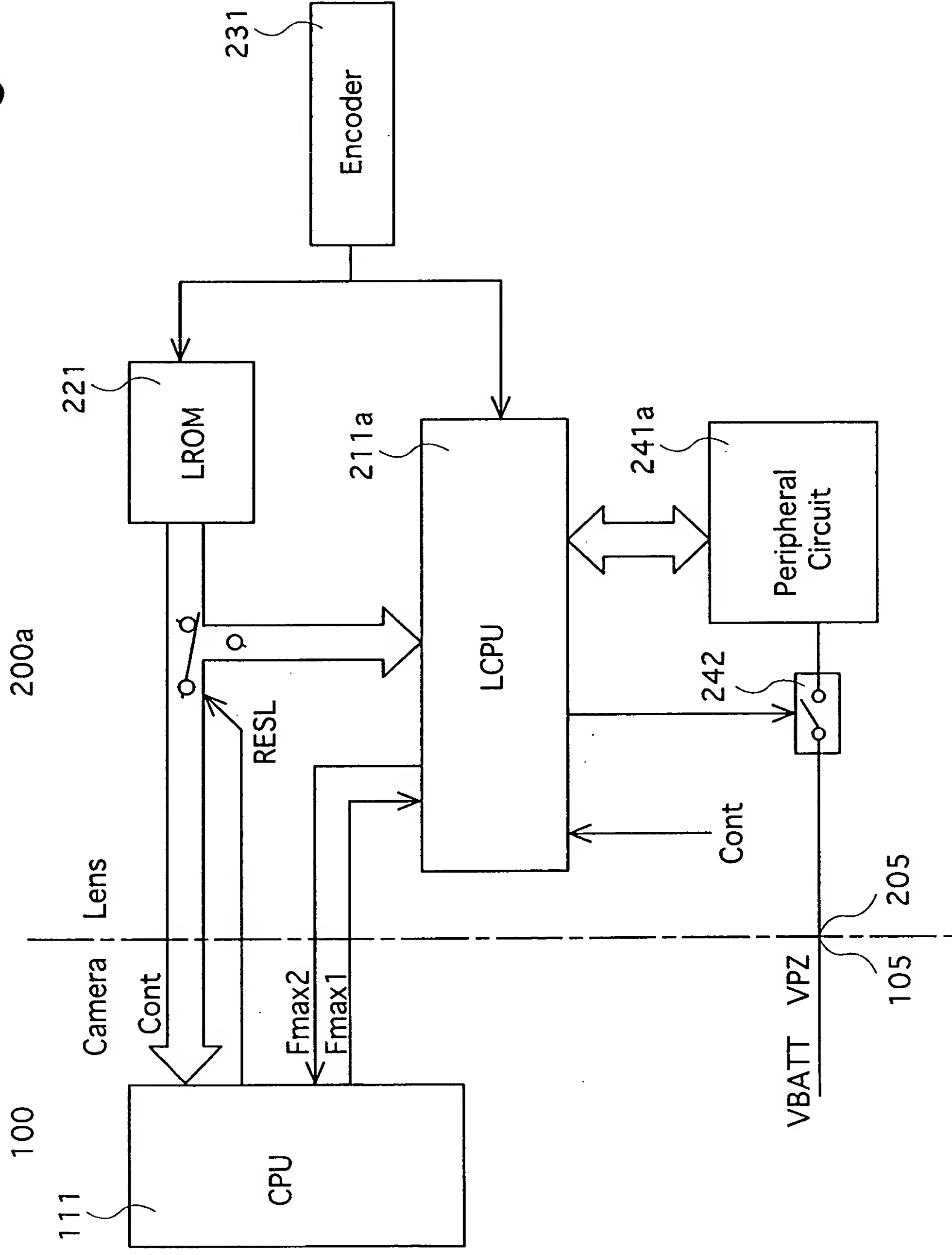


# Fig.3



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Fig.4



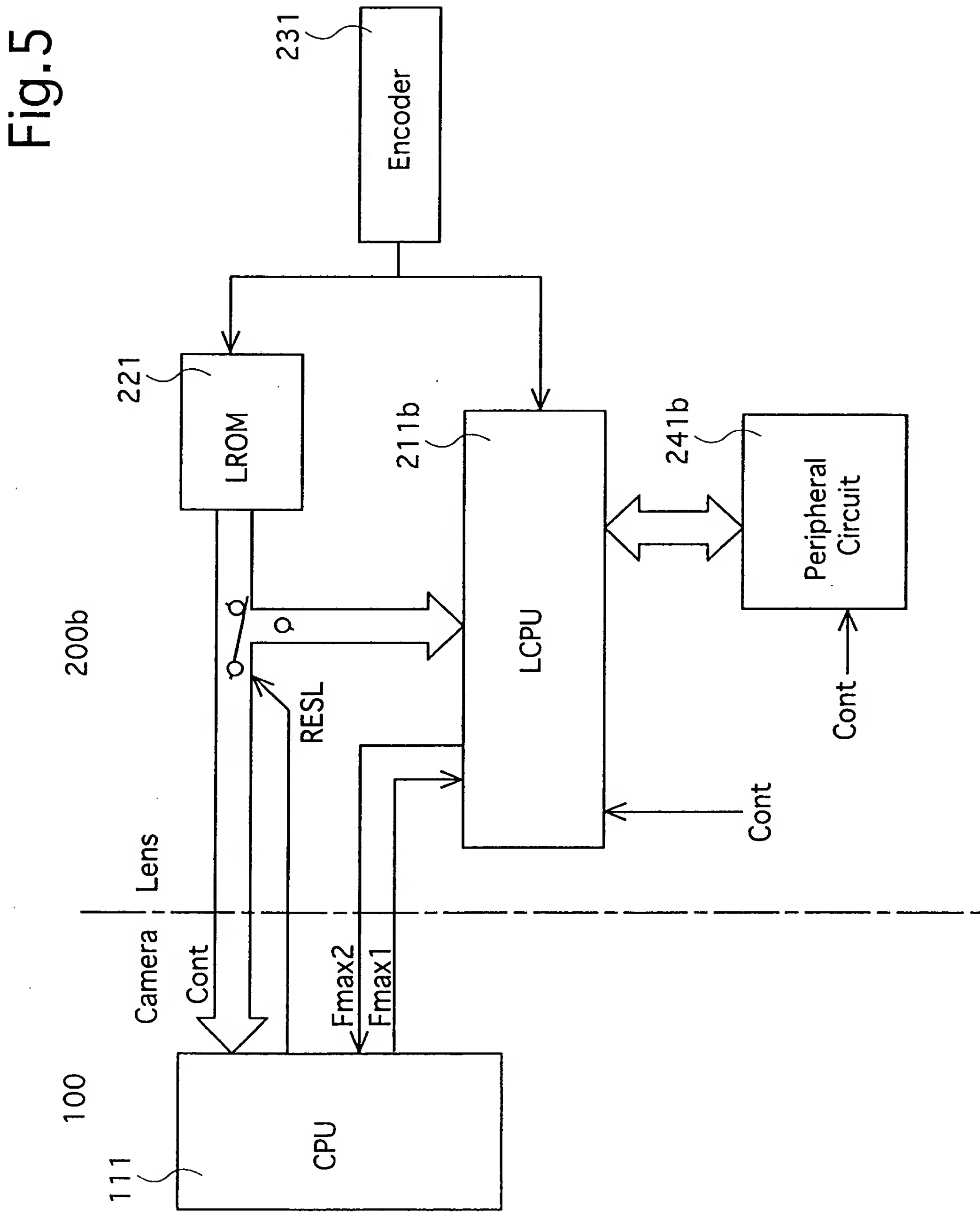
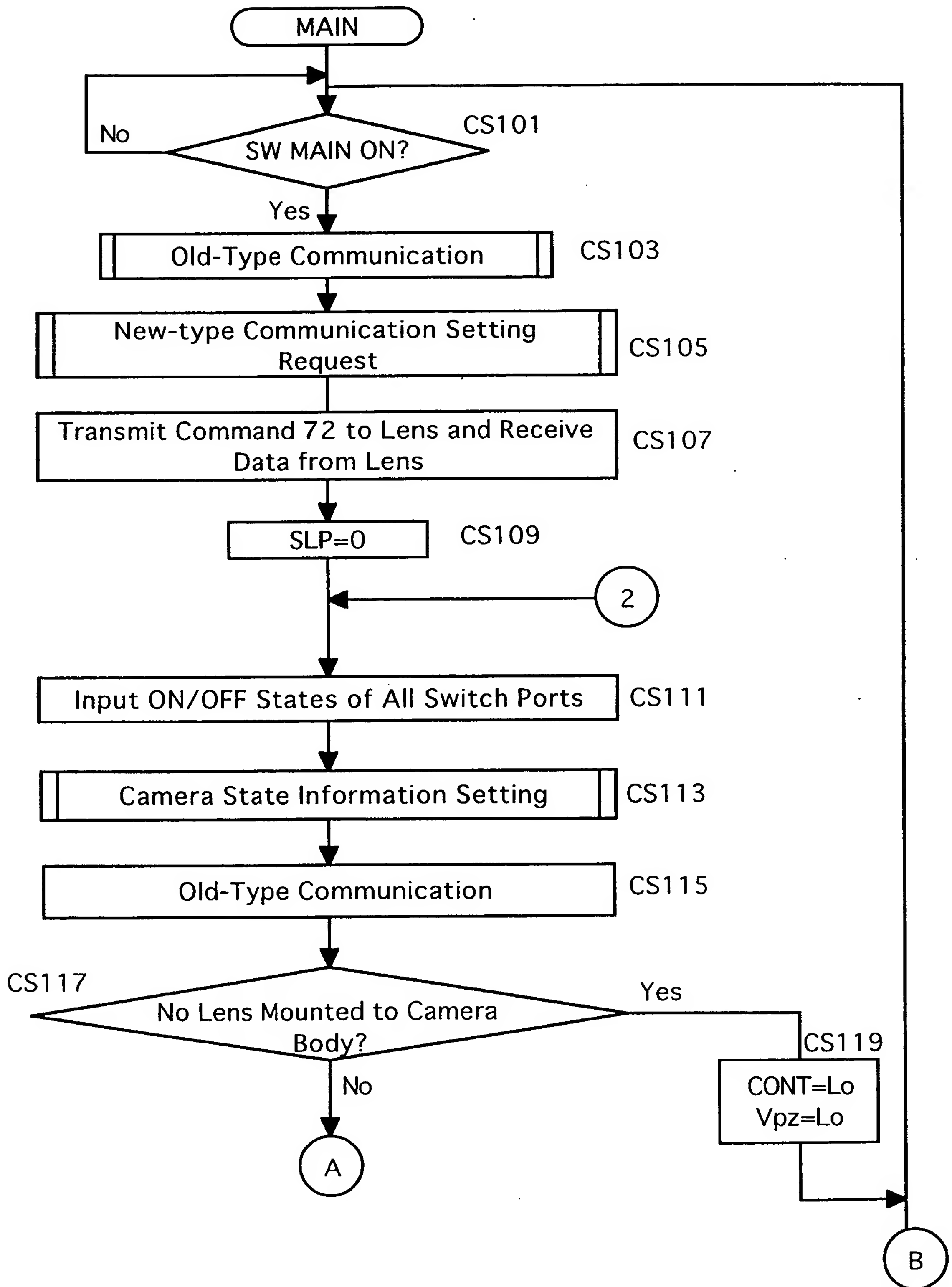


Fig.5

Fig. 6A



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Fig. 6B

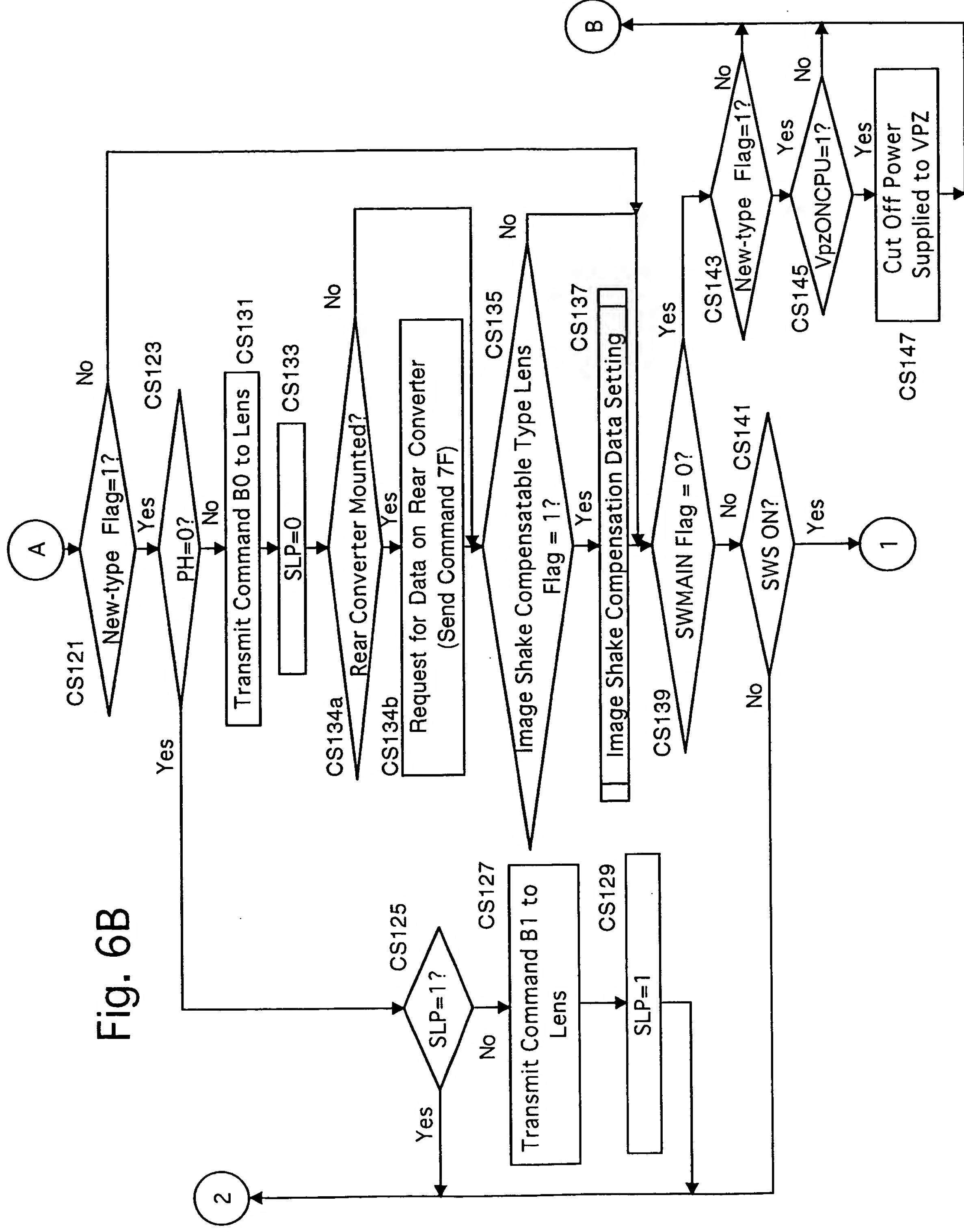
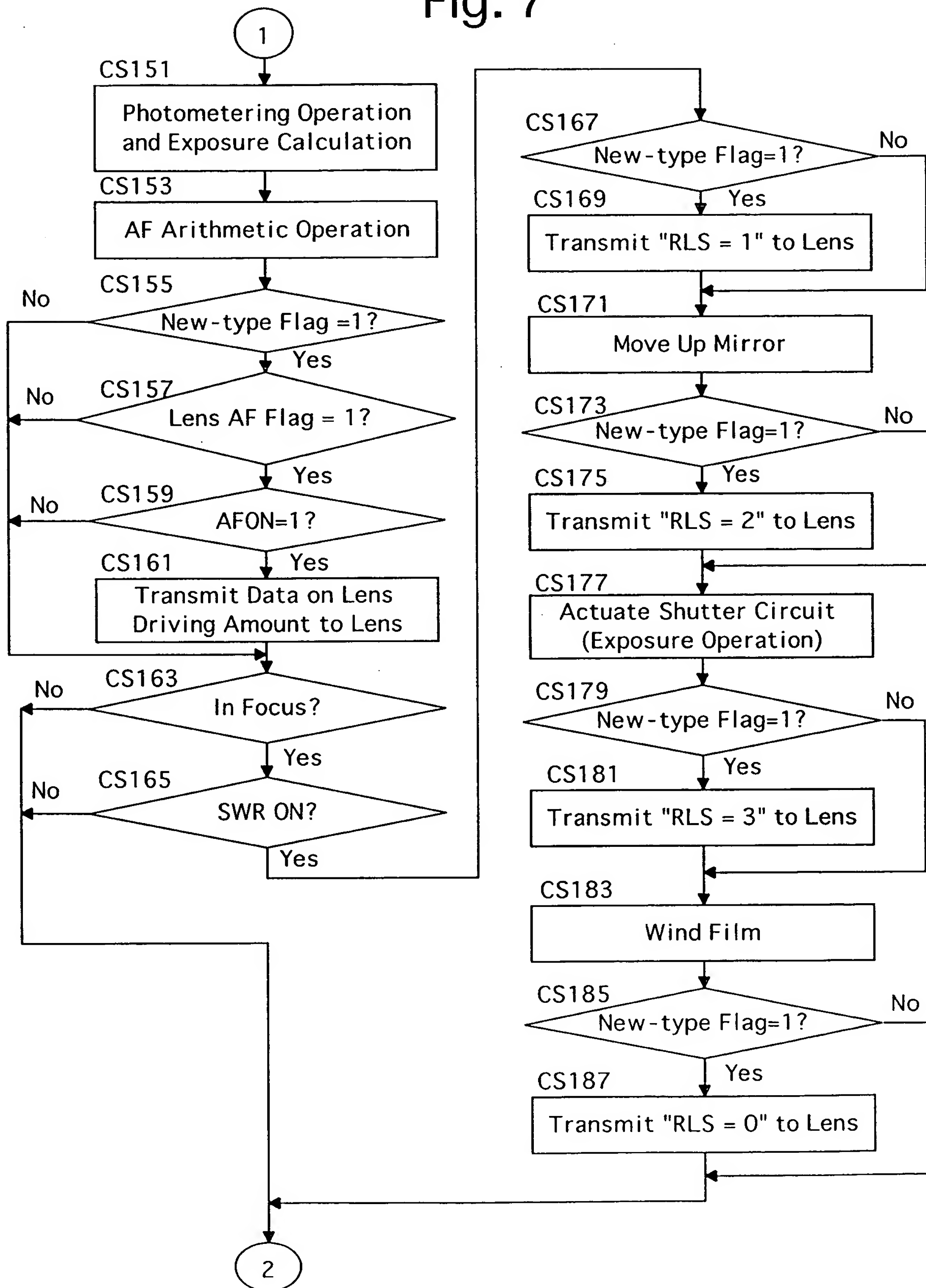


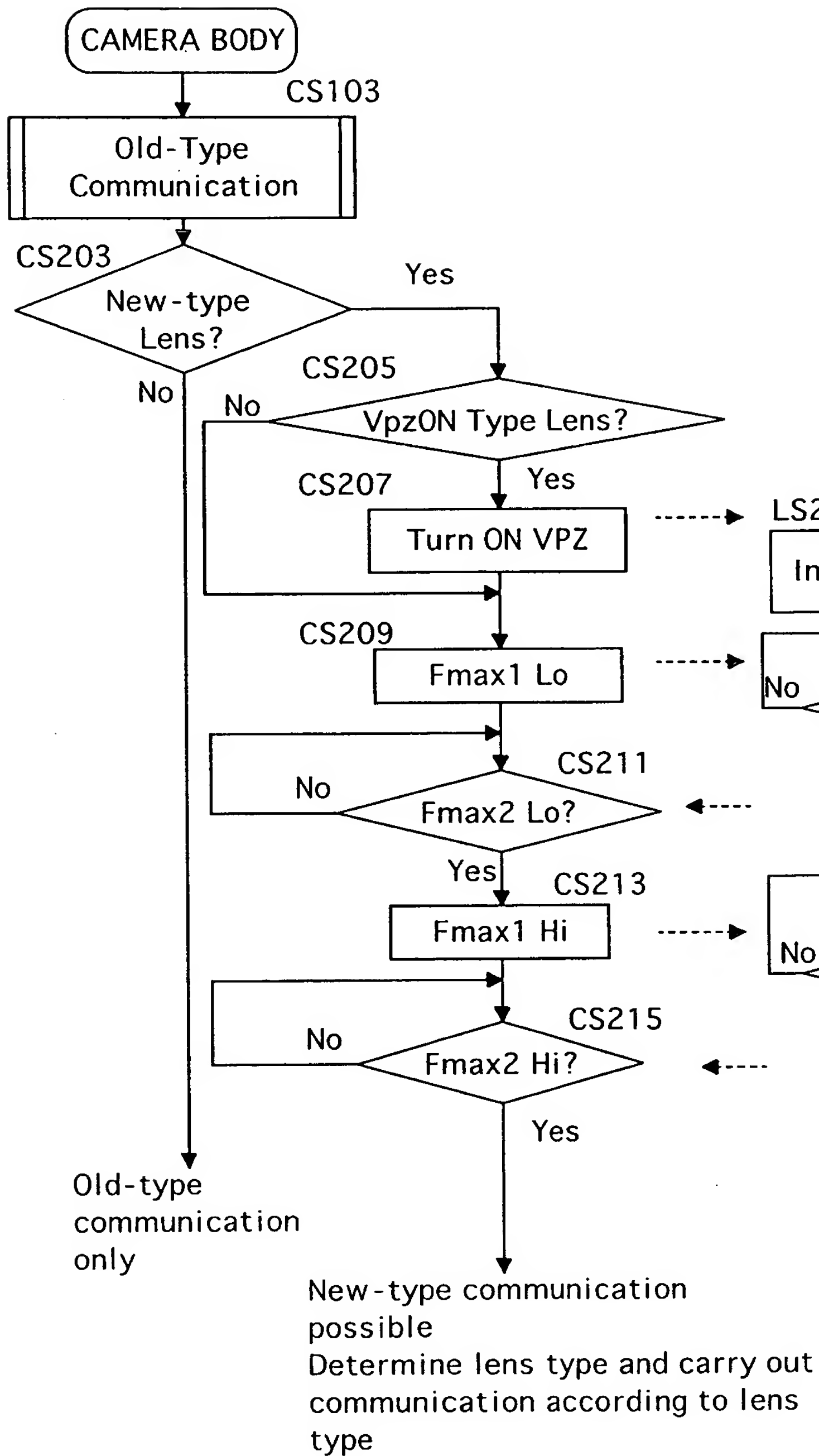
Fig. 7



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# Fig. 8A



# Fig.8B

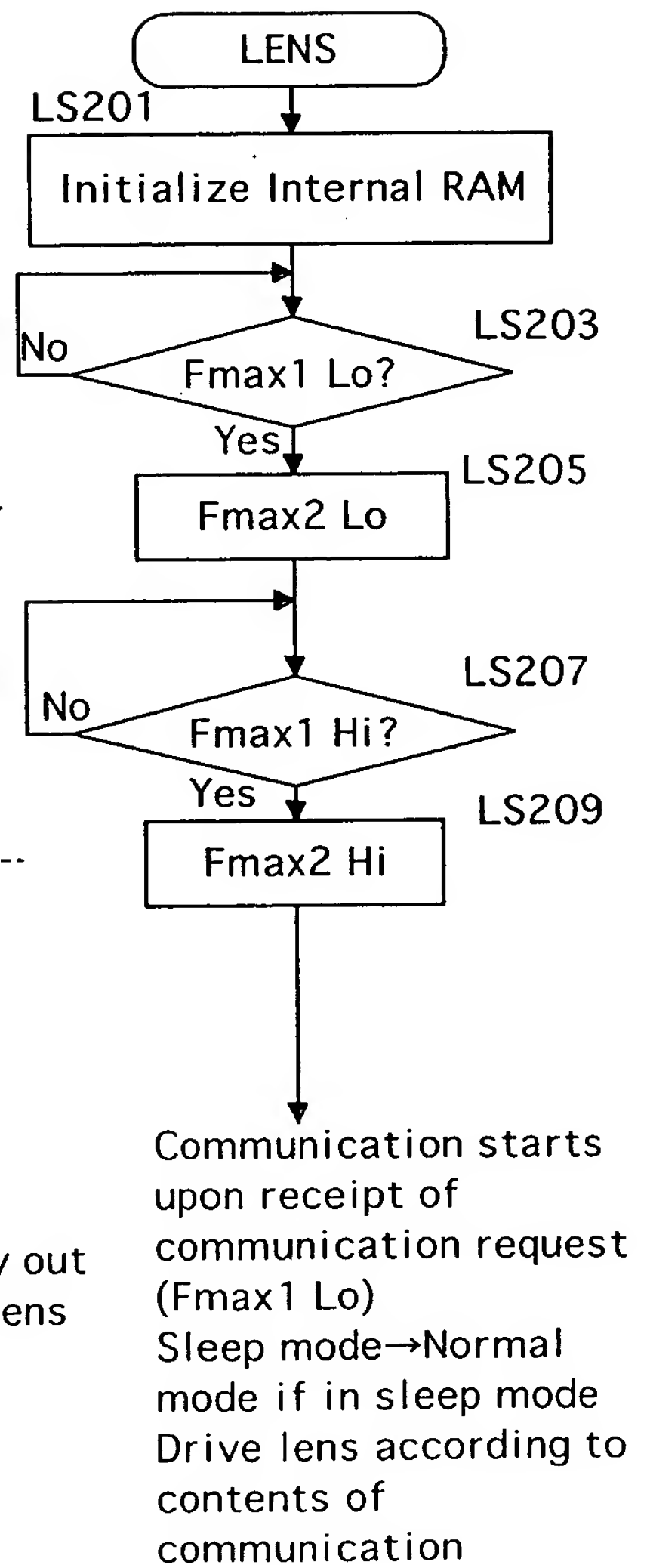


Fig.9

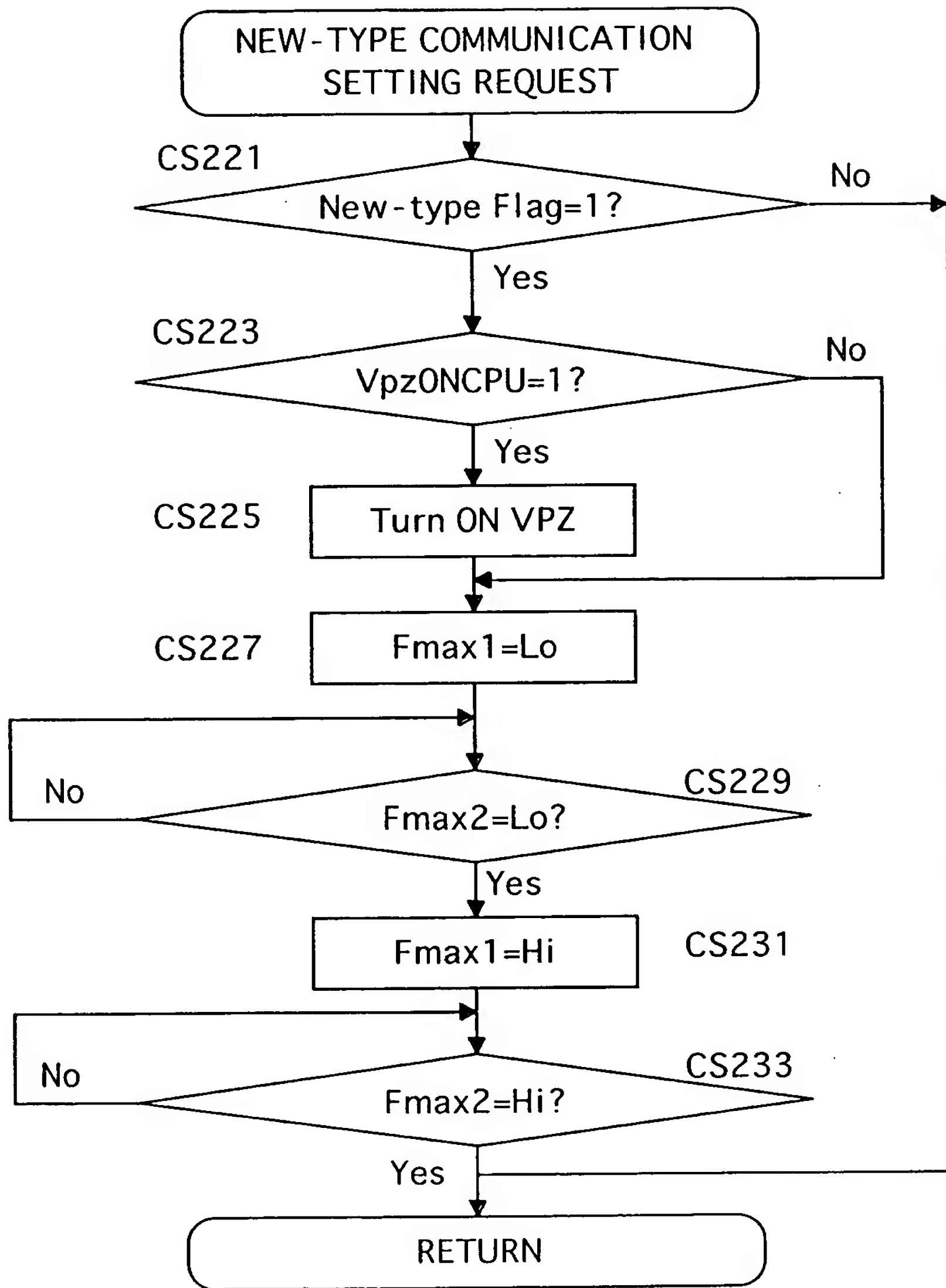
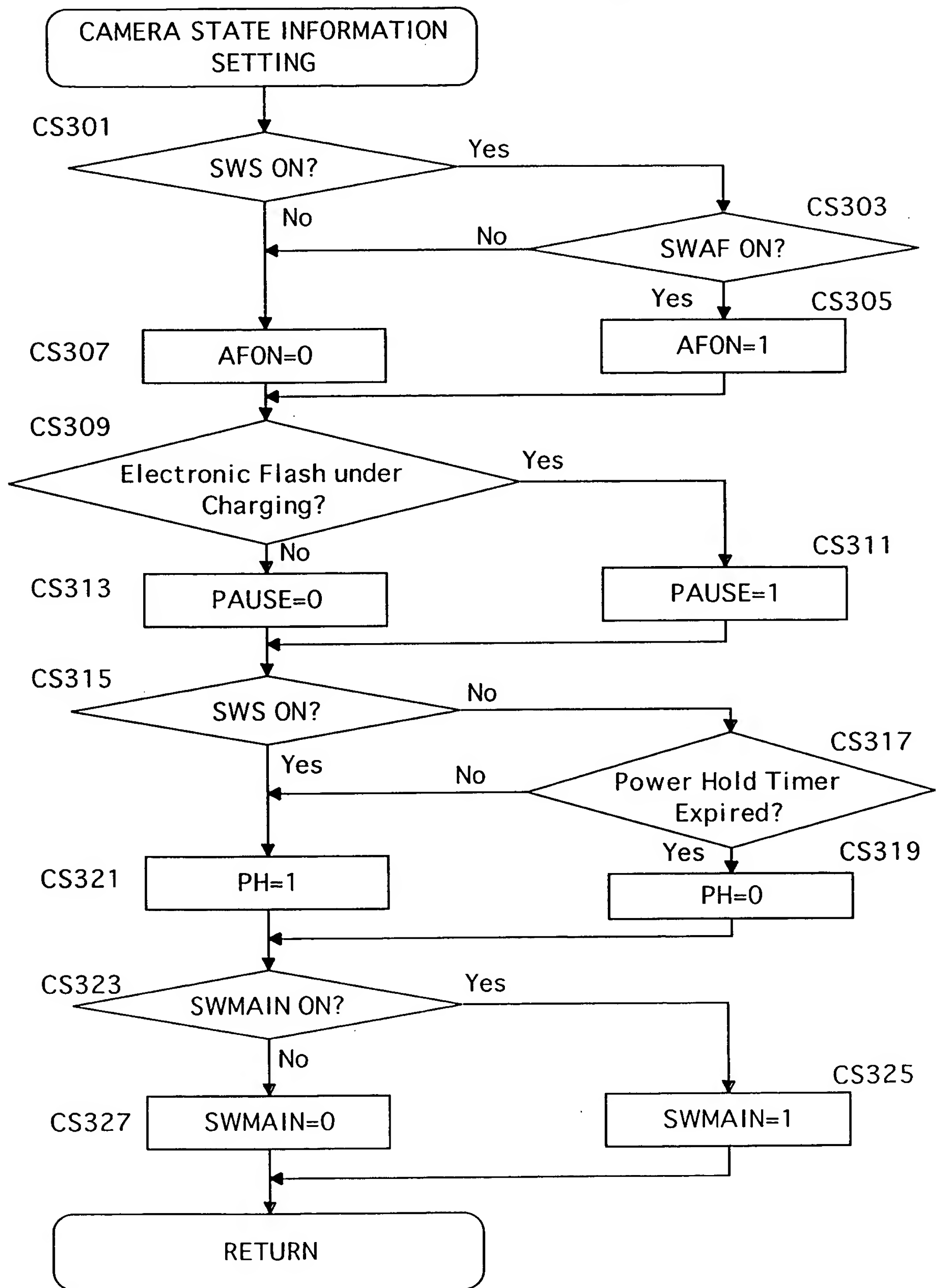


Fig.10



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Fig.11

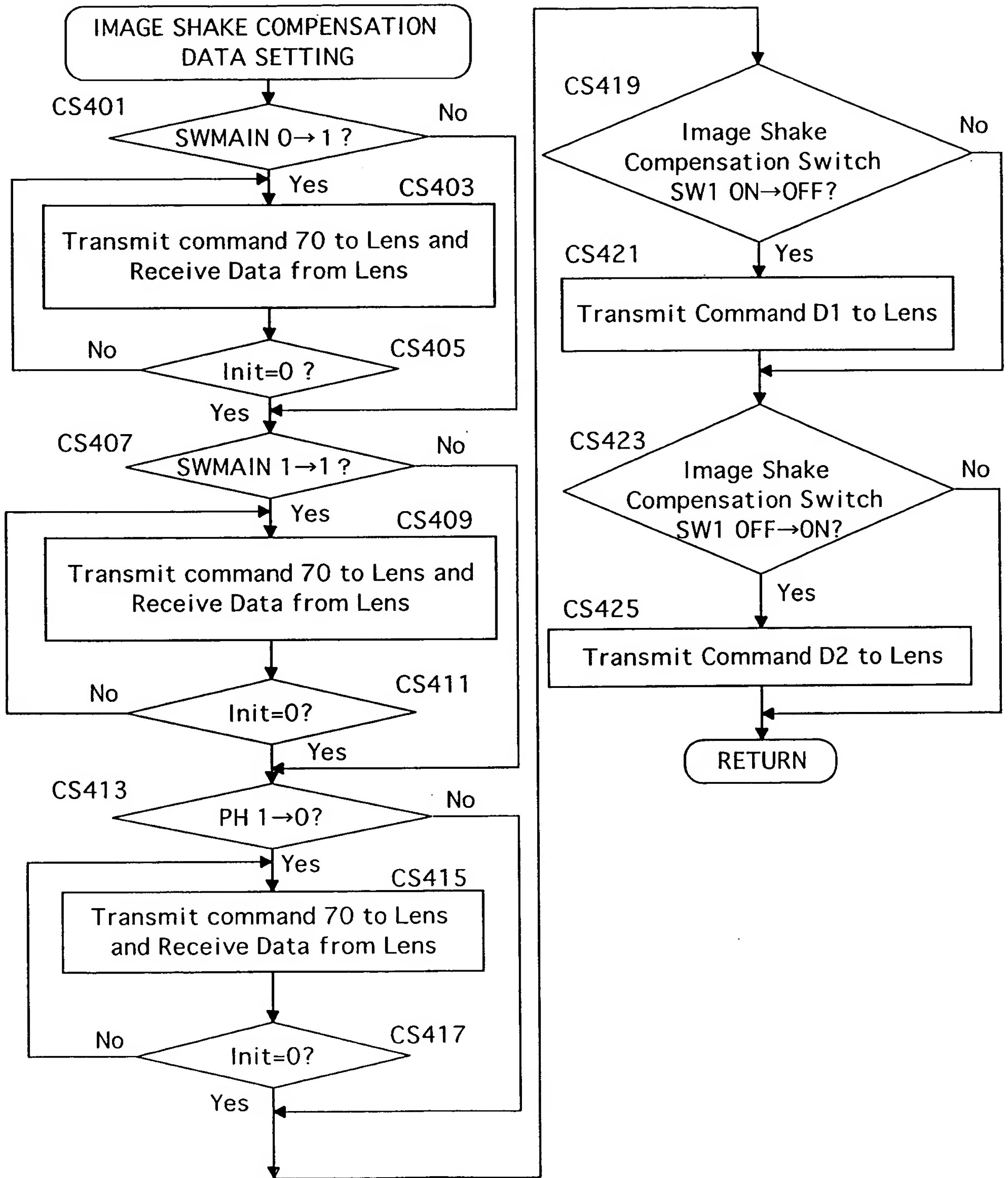


Fig. 12A

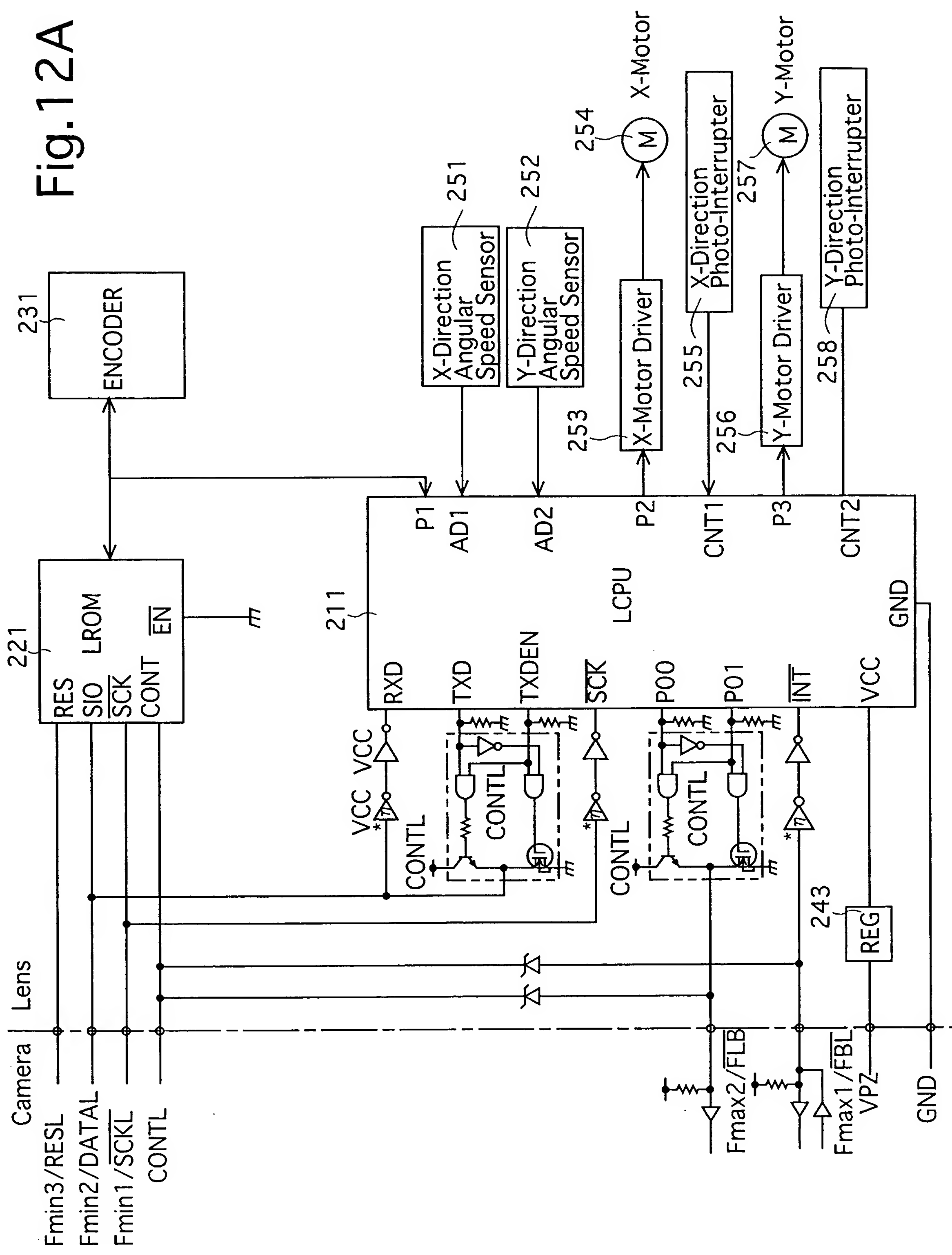


Fig. 12B

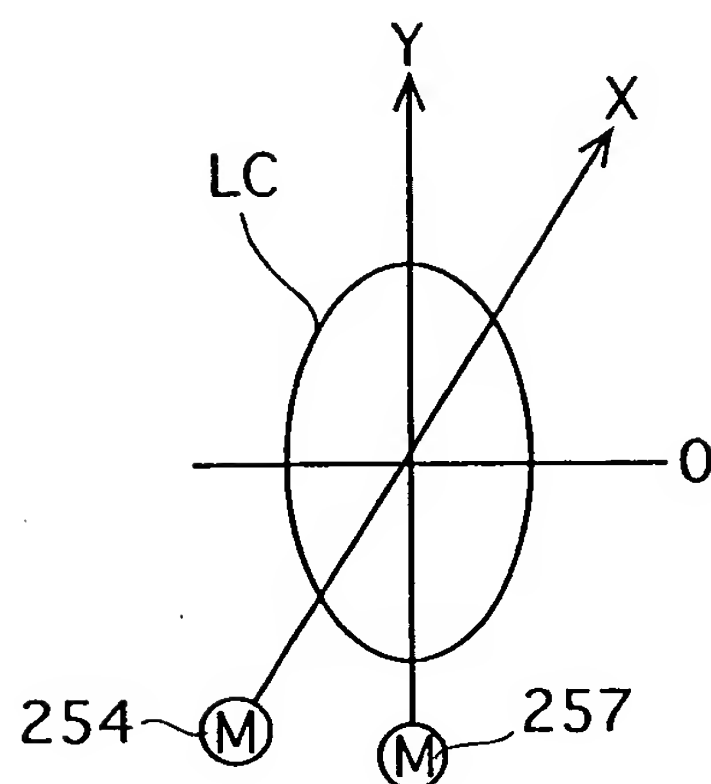


Fig.13

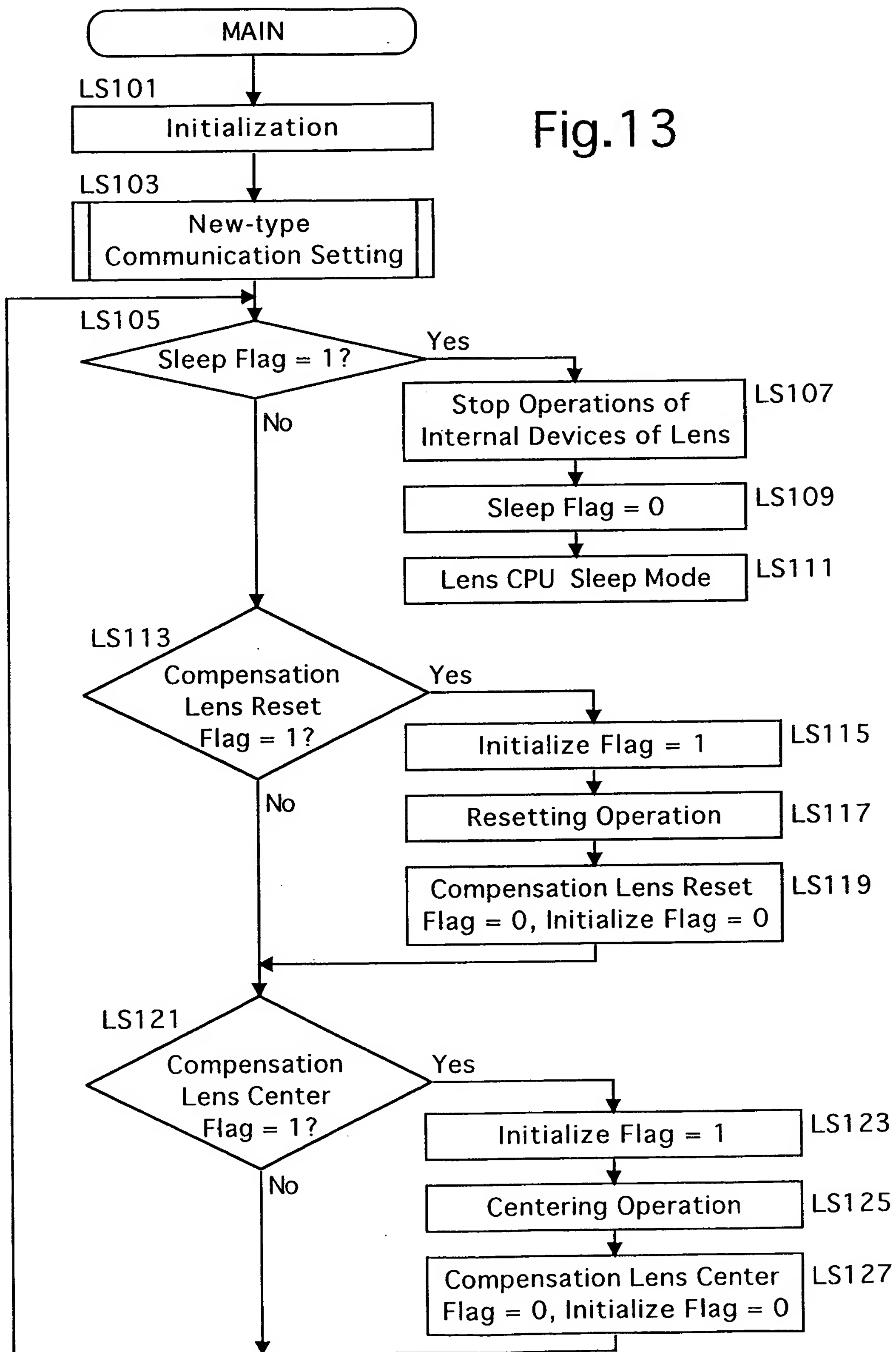
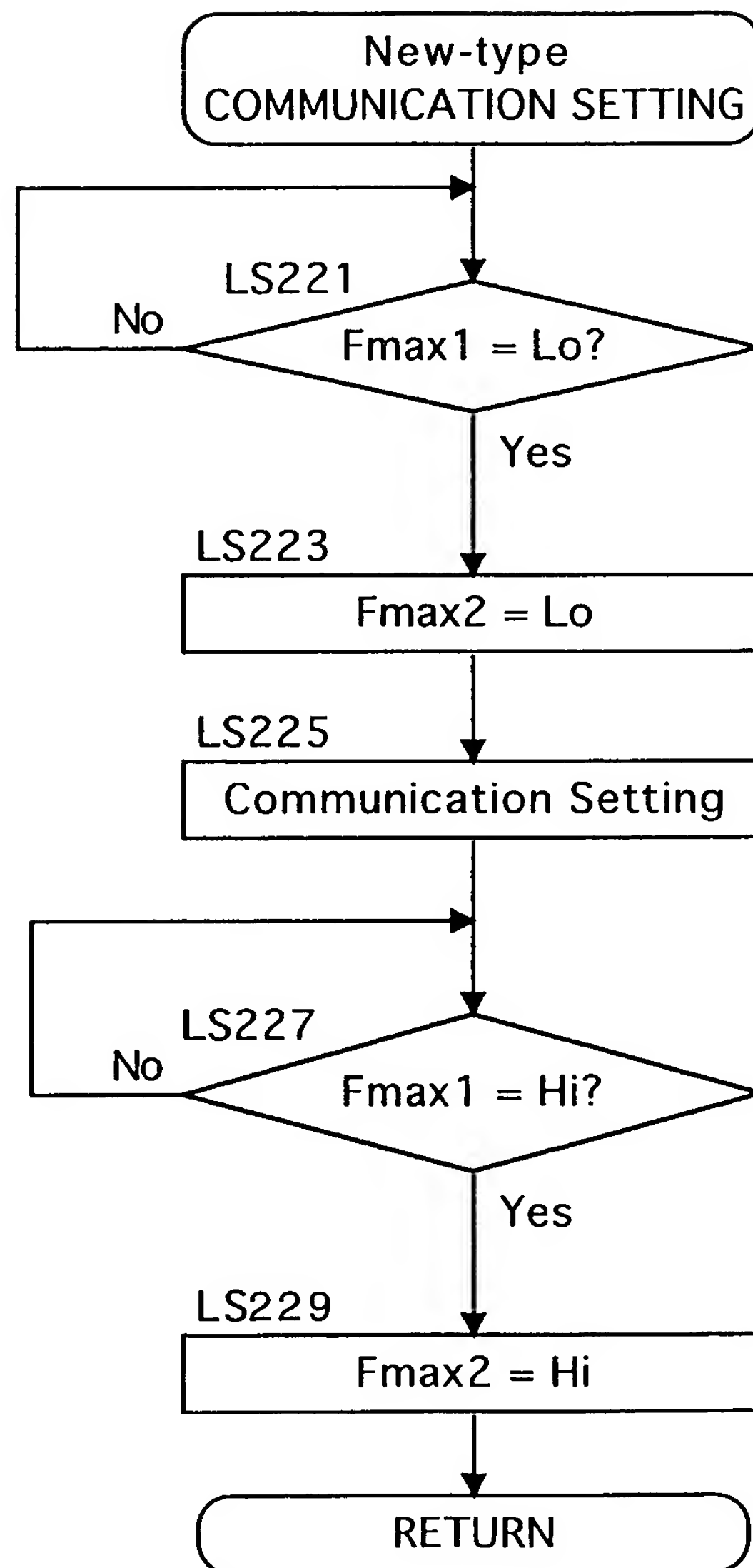


Fig.1 4



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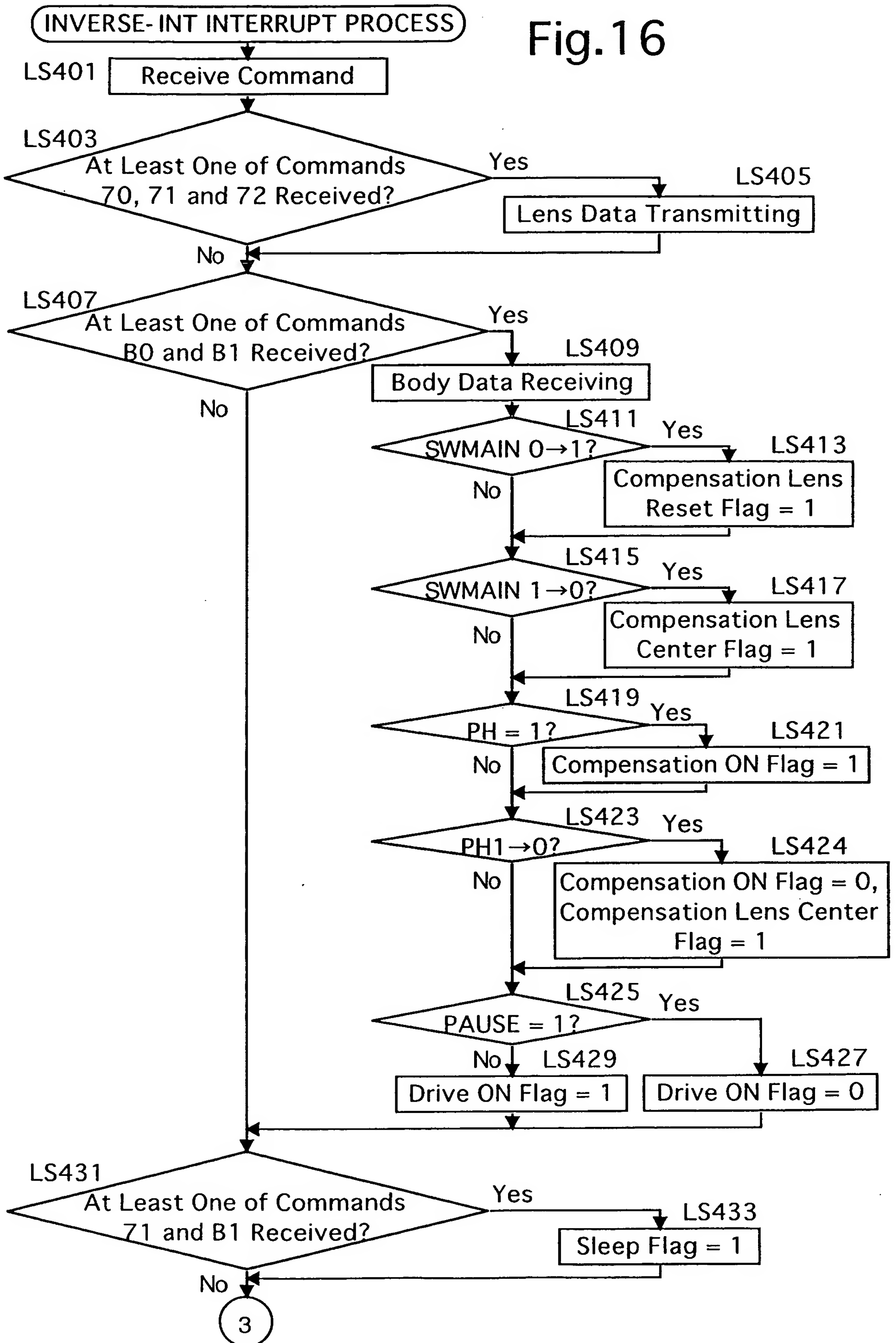


Fig.17

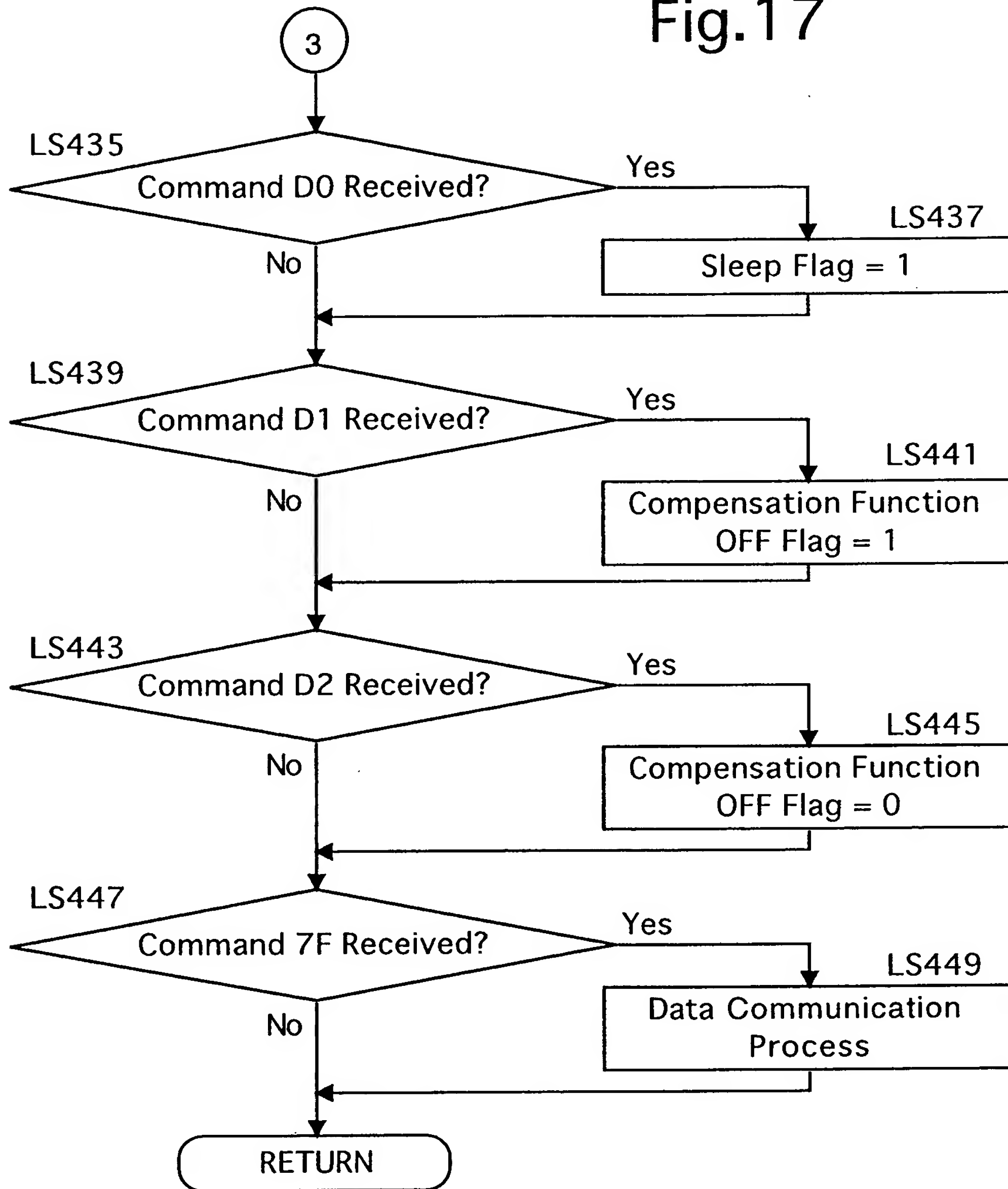


Fig. 18

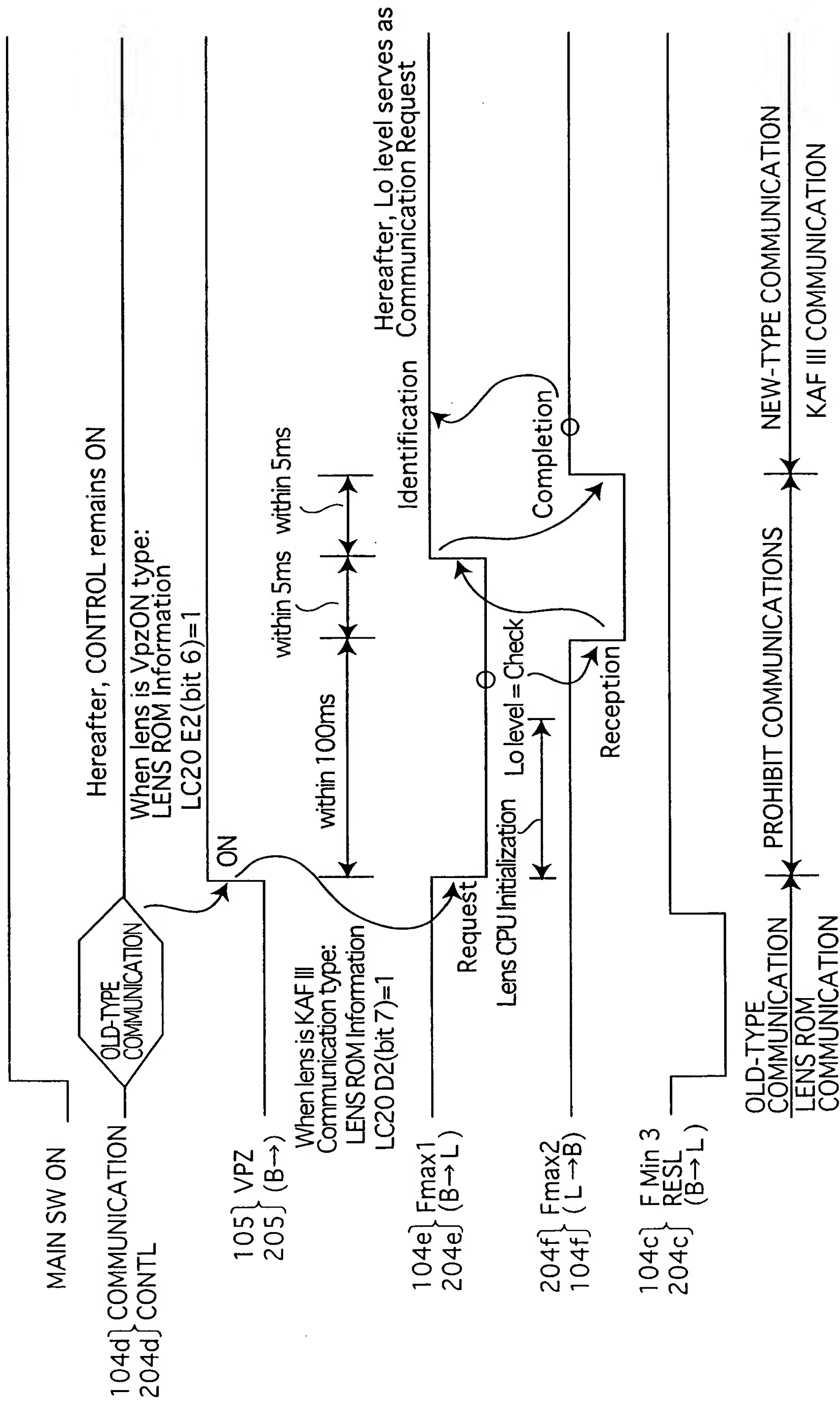


Fig. 19A

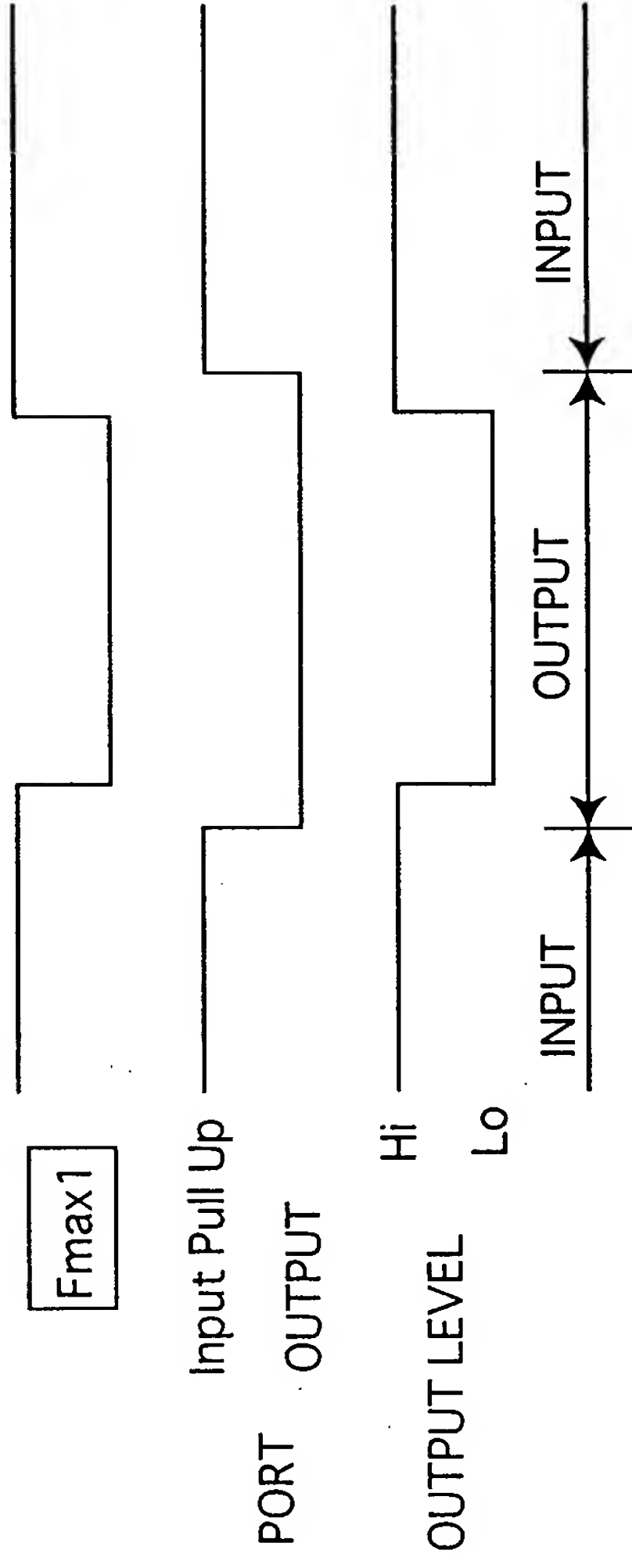


Fig. 19B

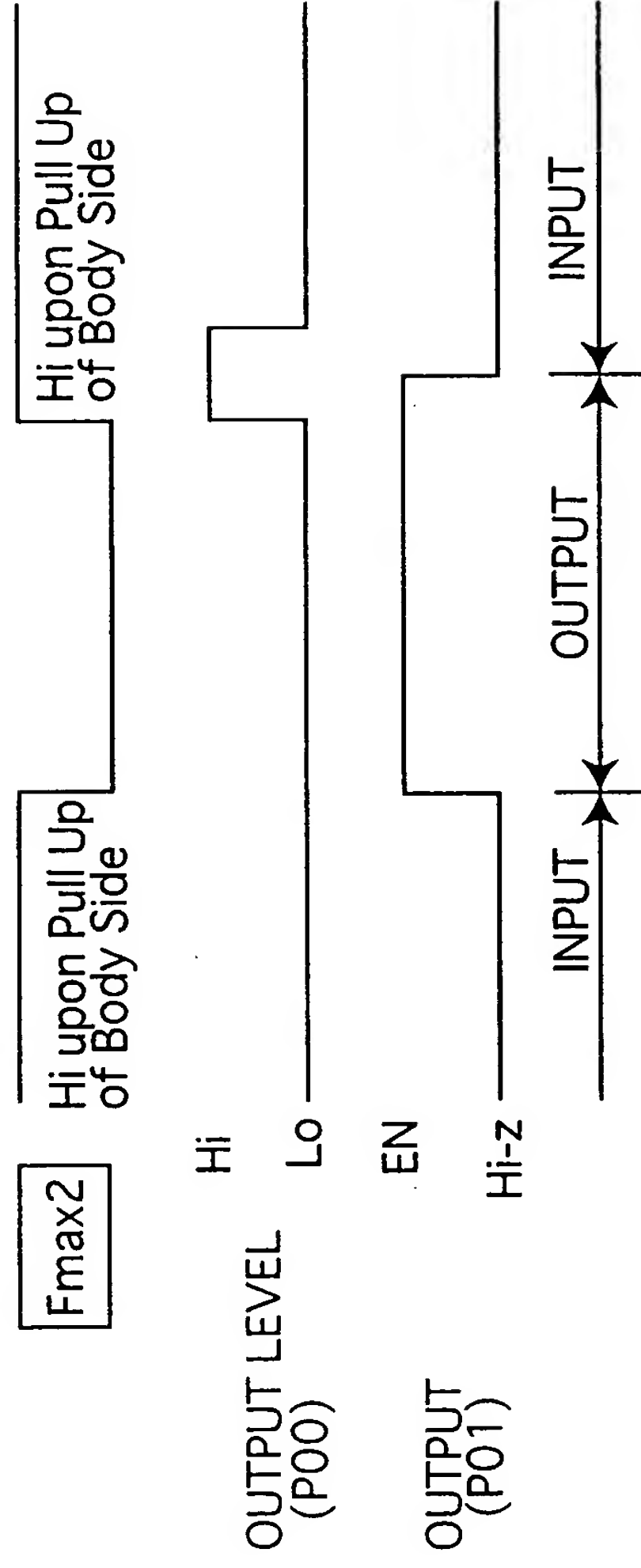


Fig. 20

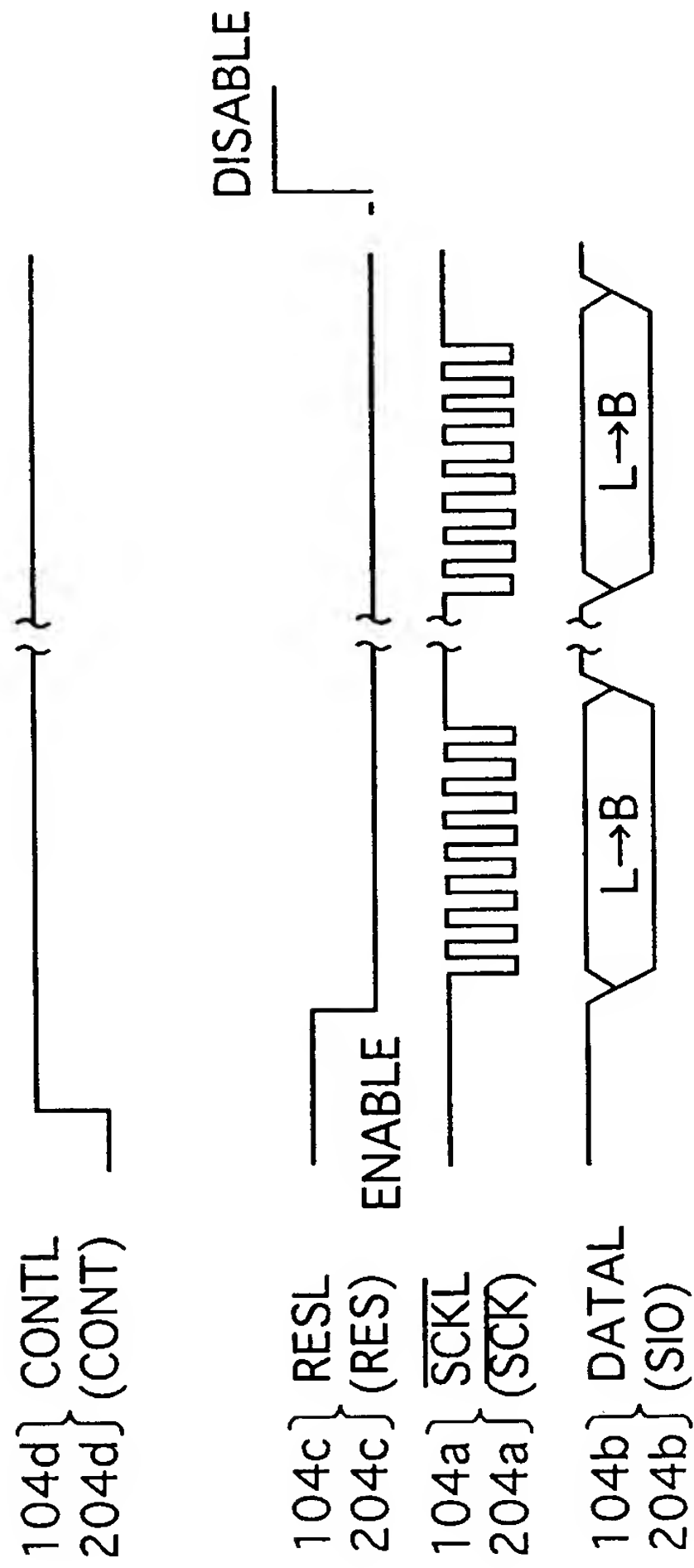
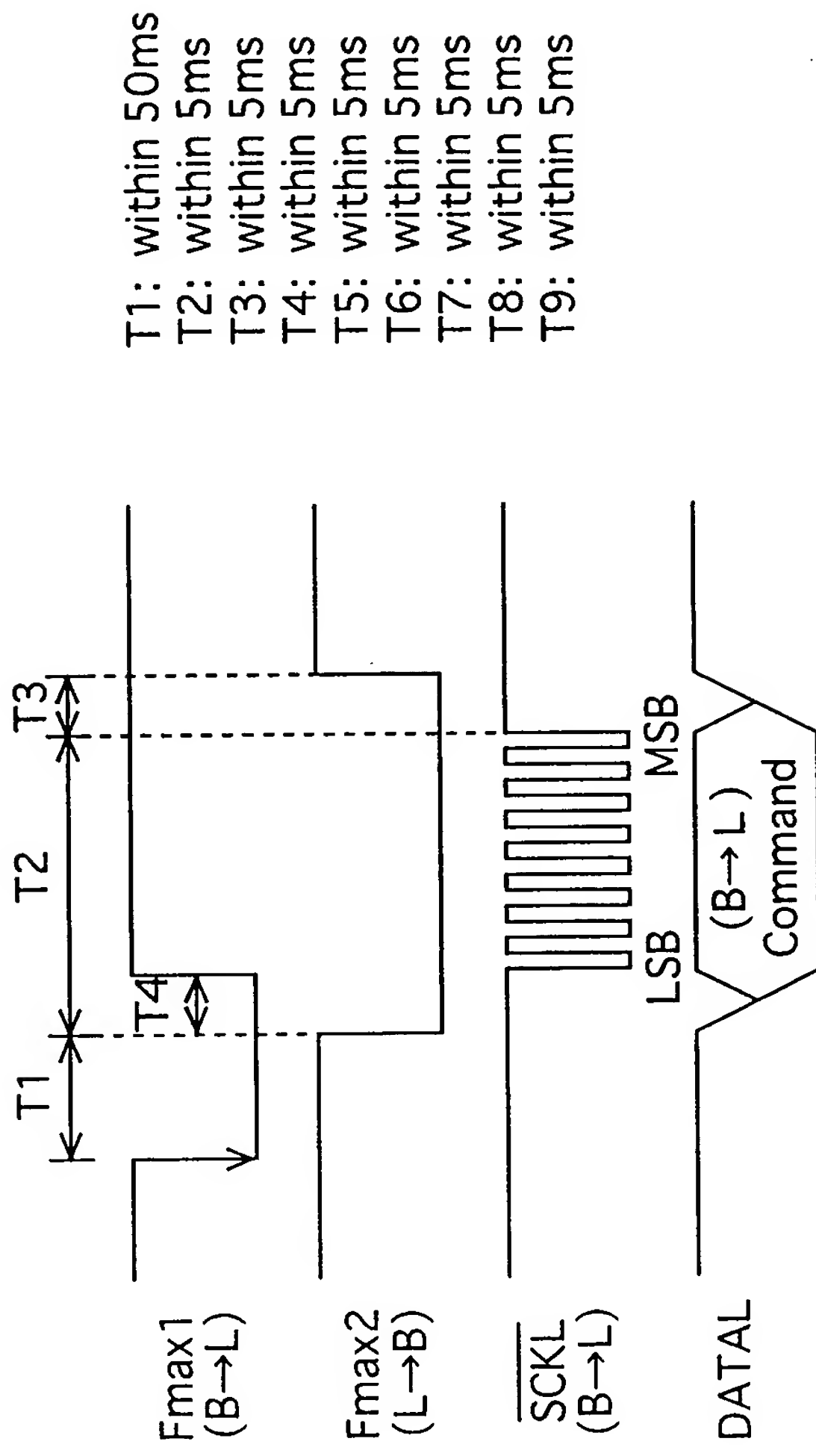


Fig.21A







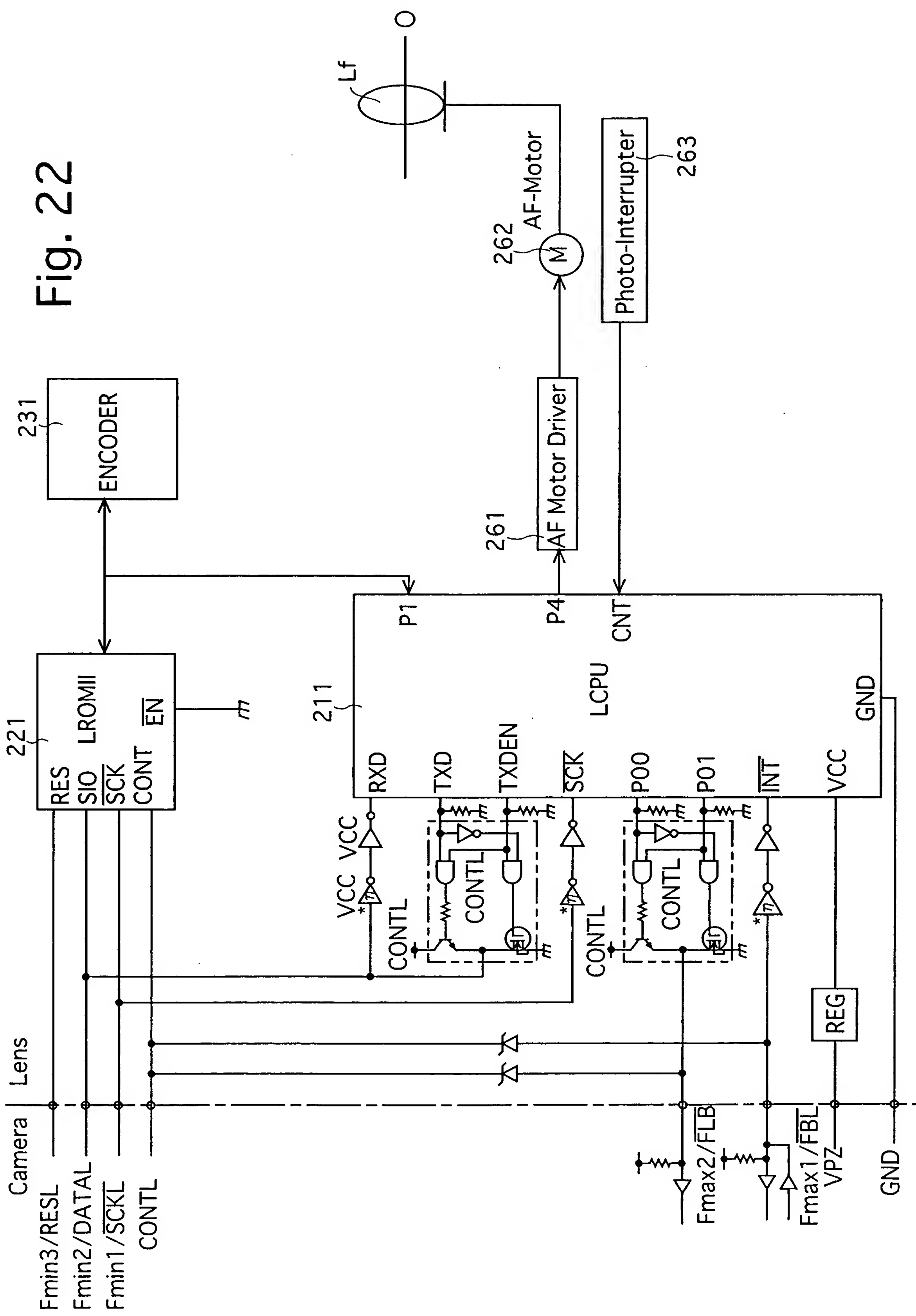
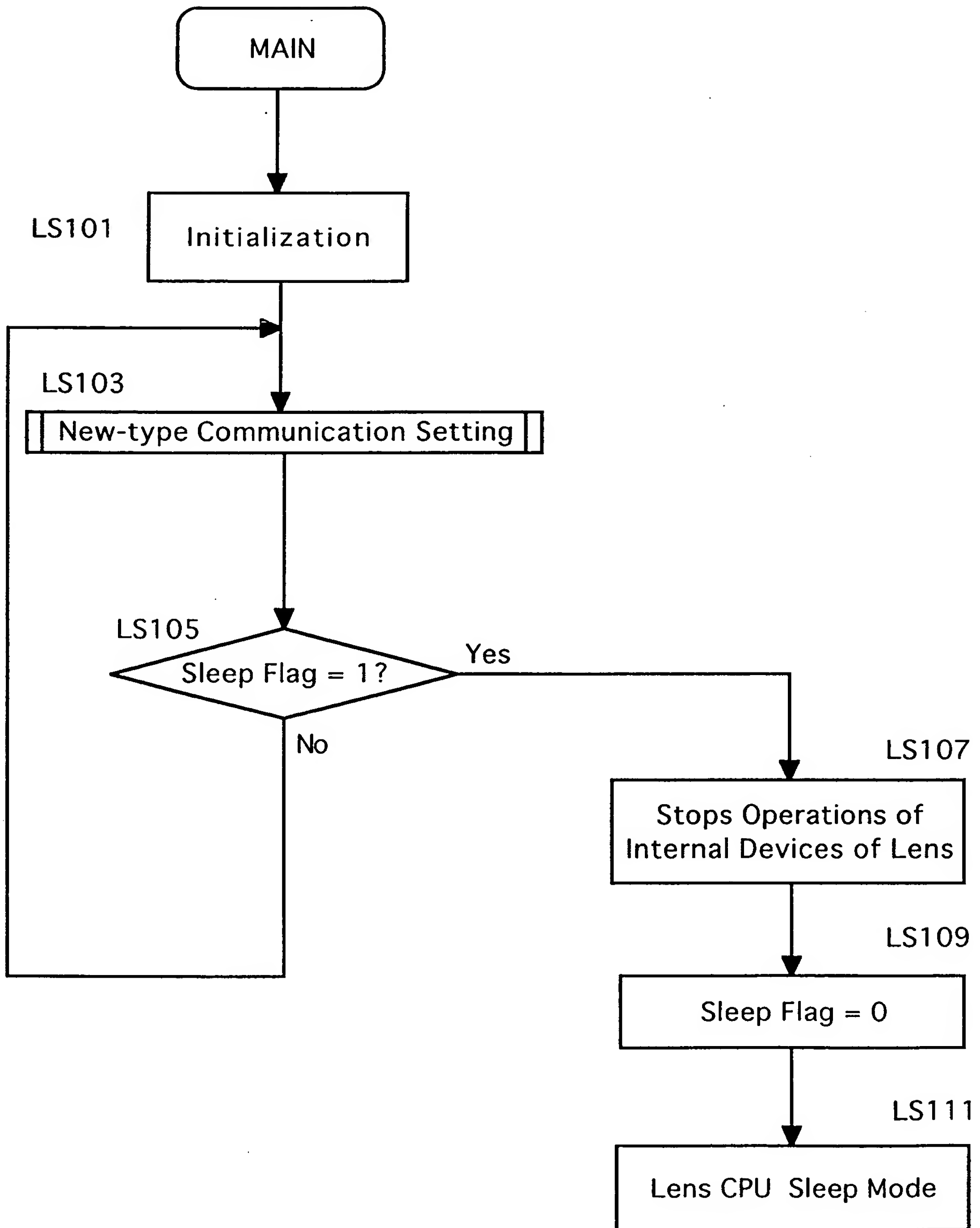
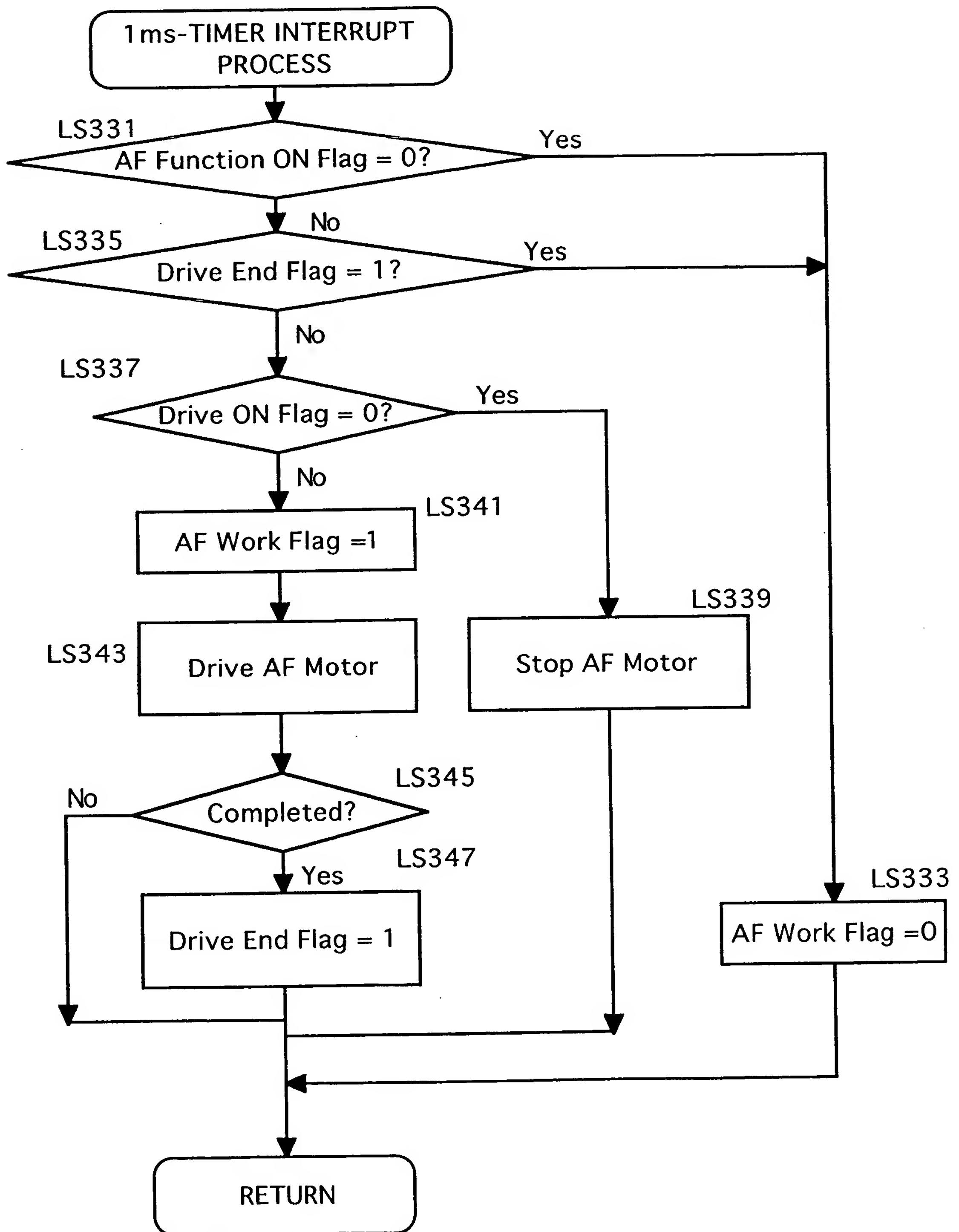


Fig. 23



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Fig. 24



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# INVERSE INT INTERRUPT PROCESS

Fig. 25

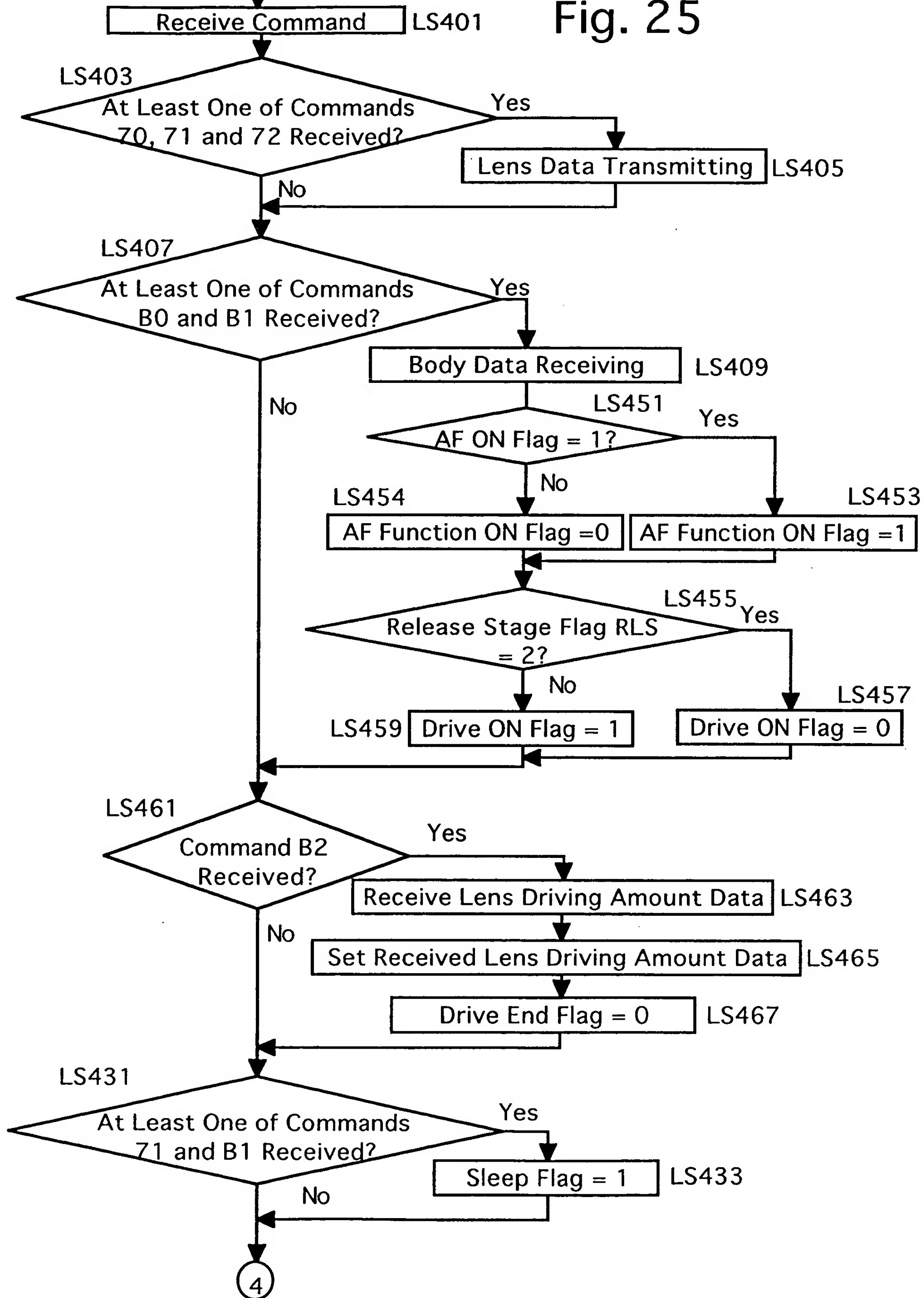
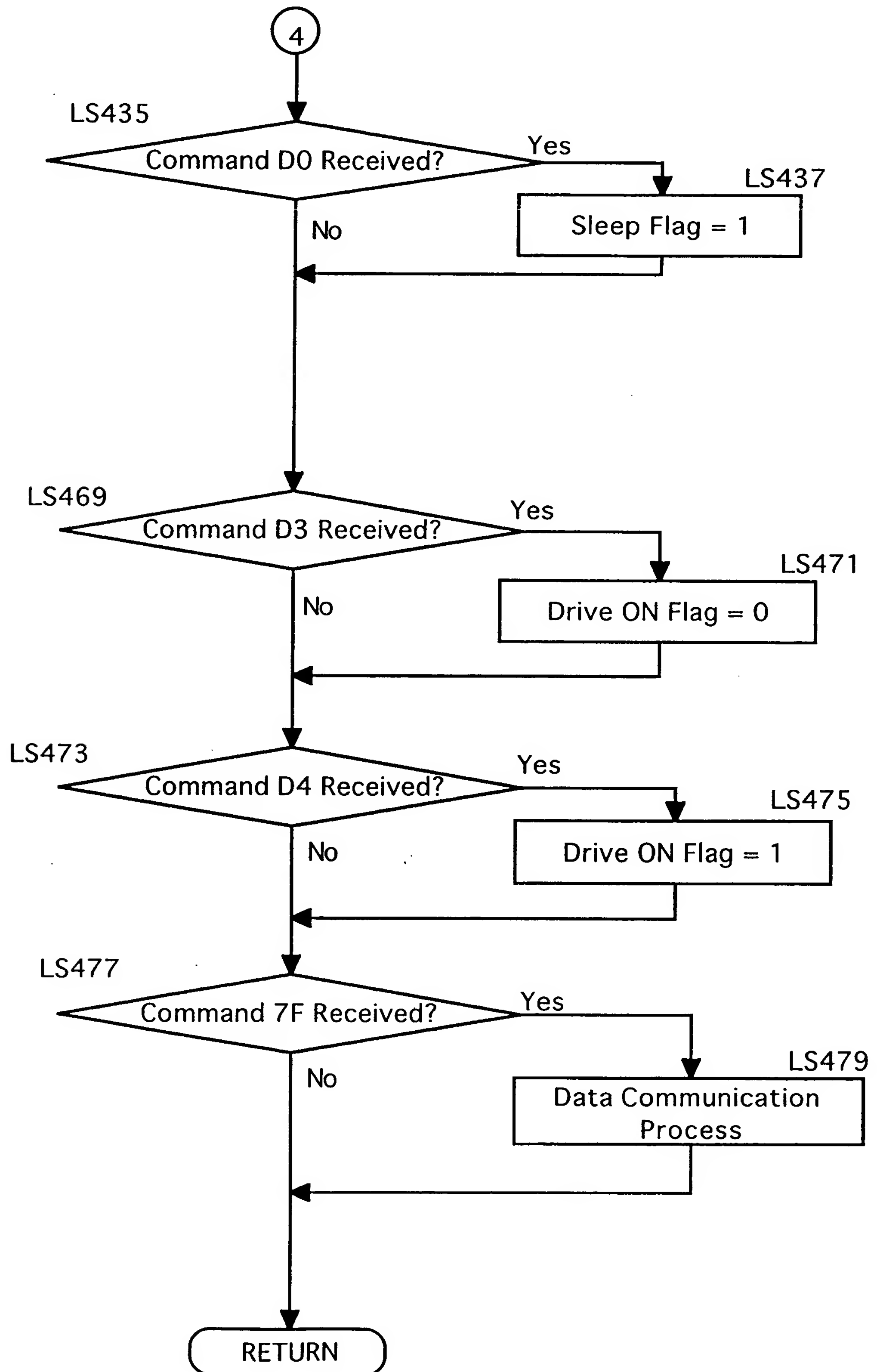


Fig. 26



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